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a plurality of drain signal lines which are formed on the liquid-crystal-side surface of one substrate such that the drain signal lines intersect the gate signal lines,

the pixel regions are regions which are surrounded by the gate signal lines which are arranged close to each other and the drain signal lines which are arranged close to each other,

the pixel regions are provided with thin film transistors which are driven with the supply of scanning signals from the gate signal lines at one side,

the pixel electrodes receive video signals from the drain signal lines at one side through thin film transistors, and

the island-like multi-layered material layer is formed of a laminated body made of at least two material layers selected from a material layer which is made of material equal to material of the gate signal lines, a material layer which is made of material equal to material of gate insulation films of thin film transistors, a material layer which is made of material equal to material of the drain signal lines, a material layer which is made of material equal to material of a protective film which covers the thin film transistors.